

Docket No.: 216780US2DIV

APR 10 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Satoshi SHIMIZU, et al.

SERIAL NO: 10/028,766

GAU: 2811

FILED: December 28, 2001

EXAMINER: NGUYEN, C.

FOR: MIS TRANSISTOR AND METHOD OF FABRICATING THE SAME

REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

This is a request for Continued Examination (RCE) under 37 C.F.R. §1.114 of the above-identified application.

Submission required under 37 C.F.R. §1.114

Previously Submitted:

- ☐ Consider the amendment(s)/reply under 37 C.F.R. §1.116 previously filed on
- ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on

Enclosed:

- ☒ Amendment w/Marked-Up Copy
- ☐ Information Disclosure Statement (IDS)
- ☐ Other:

FEES	RATE	CALCULATIONS
<input type="checkbox"/> Suspension of action on the above-identified application is requested under 37 C.F.R. §1.103(c) for a period of _____ months.	\$130.00	\$0.00
<input checked="" type="checkbox"/> RCE Fee required under 37 C.F.R. §1.17(e)	\$750.00	\$750.00
<input type="checkbox"/>		\$0.00
<input type="checkbox"/>		\$0.00
TOTAL OF ABOVE CALCULATIONS:		\$750.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING AS SMALL ENTITY		\$0.00
TOTAL:		\$750.00

☒ A check in the amount of \$750.00 is enclosed

☒ Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 CFR 1.136, and any additional fees required under 37 CFR 1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate of this sheet is enclosed.

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Respectfully Submitted,

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#9
RCE
y/Robinson
4/22/05

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TECHNOLOGY CENTER 2800

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#10/D
Amend D
Y. Robinson
4/22/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

SATOSHI SHIMIZU ET AL.

SERIAL NO.: 10/028,766

RCE FILED: HERewith

FOR: MIS TRANSISTOR AND METHOD OF
FABRICATING THE SAME

:

: GROUP ART UNIT: 2811

:

: EXAMINER: NGUYEN, C.

AMENDMENT FILED CONCURRENTLY WITH
REQUEST FOR CONTINUED EXAMINATION (RCE)

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action of January 10, 2003, please amend the above-
identified application as follows:

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IN THE CLAIMS

Please amend Claim 10 to read as follows:¹

10. (Twice Amended) A MIS transistor including:
- a gate electrode being formed to be opposed to a silicon substrate through a gate insulating film;
 - first side walls being insulative and formed on said silicon substrate on both sides of said gate electrode, said first side walls being higher than said gate electrode from said silicon substrate;

¹A marked-up copy of the amendments is attached hereto.

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